

WHAT IS CLAIMED IS:

1. A method of transmitting signals in a network comprising:
receiving a first signal and a second adjacent signal;
sensing simultaneous transition of the first signal and the second signal;
delaying the first signal for a time period;
switching the second signal; and
switching the first signal after the time period.
2. The method of claim 1 wherein
sensing is performed by a sensing and delay circuit.
3. The method of claim 1 wherein
the first signal is delayed by a first buffer before switching; and
the second signal is delayed by a second buffer before switching.
4. The method of claim 3 wherein
sensing is performed by a sensing and delay circuit.
5. The method of claim 4 wherein
the sensing and delay circuit provides a delay signal to the first buffer and delays the
first signal until after the second signal switches.
6. The method of claim 1 further comprising:
receiving a third signal wherein the first signal is adjacent to the second and the third
signal;
sensing simultaneous transition of the third signal with the first signal;
delaying the first signal for a time period;
switching the third signal; and
switching the first signal after the time period.
7. The method of claim 6 wherein
sensing is performed by a sensing and delay circuit.

1 8. The method of claim 7 wherein
2 the first signal is delayed by a first buffer before switching;
3 the second signal is delayed by a second buffer before switching, and
4 the third signal is delayed by a third buffer before switching.

1 9. The method of claim 8 wherein
2 sensing is performed by a sensing and delay circuit.

1 10. The method of claim 9 wherein
2 the sensing and delay circuit provides a delay signal to the first buffer and delays the
3 first signal until after the second signal and third signal switches.

1 11. A method of transmitting signals in a network comprising:
2 receiving a first, a second, a third, a fourth, and a fifth adjacent signals;
3 sensing transitions of the first, the second, the third, the fourth, and the fifth signals;
4 delaying switching of the first signal in the event the second signal transitions at the
5 same time as the first signal;
6 delaying switching of the third signal in the event the second and fourth signals
7 transition at the same time as the third signal; and
8 delaying switching of the fifth signal in the event the fourth signal transitions at the
9 same time as the fifth signal.

1 12. The method of claim 11 wherein
2 sensing is performed by a sensing and delay circuit.

1 13. The method of claim 11 wherein
2 the first signal is delayed by a first buffer before switching;
3 the second signal is delayed by a second buffer before switching;
4 the third signal is delayed by a third buffer before switching;
5 the fourth signal is delayed by a fourth buffer before switching; and
6 the fifth signal is delayed by a fifth buffer before switching.

1 14. The method of claim 13 wherein
2 sensing is performed by a sensing and delay circuit.

1 15. The method of claim 14 wherein
2 the sensing and delay circuit provides a delay signal to the first buffer and delays the
3 first signal until the second signal switches;
4 the sensing and delay circuit provides a delay signal to the third buffer and delays the
5 third signal until the second and the fourth signal switch; and
6 the sensing and delay circuit provides a delay signal to the fifth buffer and delays the
7 fifth signal until the fourth signal switches.

16. A signal transmitting network comprised of
a device configured to:
receive a first signal and a second adjacent signal;
sense simultaneous transition of the first signal and the second signal;
delay the first signal for a time period;
switch the second signal; and
switch the first signal after the time period.

17. A signal transmitting network comprised of:
a first device configured to:
receive a first signal and a second adjacent signal;
delay the first signal for a time period;
switch the second signal; and
switch the first signal after the time period; and
a second device configured to:
sense simultaneous transition of the first signal and the second signal.

18. The signal transmitting network of claim 16 further comprised of:
a first buffer that delays the first signal before switching; and
a second buffer that delays the second signal before switching.

1 19. The signal transmitting network of claim 18 wherein
2 sensing is performed by a sensing and delay circuit.

1 20. The signal transmitting network of claim 19 wherein
2 the second device provides a delay signal to the first buffer and delays the first signal
3 until after the second signal switches.

1 21. The signal transmitting network of claim 16 wherein the device is further
2 configured to:
3 receive a third signal wherein the first signal is adjacent to the second and third signal;
4 sense simultaneous transition of the third signal with the first signal;
5 delay the first signal for a time period;
6 switch the third signal; and
7 switch the first signal after the time period.

1 22. A signal transmitting network comprised of:
2 a first device configured to:
3 receive a first signal, a second signal, and third signal wherein the first signal
4 is adjacent to the second signal and the third signal;
5 delay the first signal for a time period; and
6 switch the third signal, and the first signal after the time period; and
7 a second device configured to:
8 sense simultaneous transitions of the first signal and the third signal.

1 23. The signal transmitting network of claim 22 wherein
2 the first signal is delayed by a first buffer before switching;
3 the second signal is delayed by a second buffer before switching, and
4 the third signal is delayed by a third buffer before switching.

1 24. The signal transmitting network of claim 23 wherein the
2 second device is a sensing and delay circuit.

1 25. The signal transmitting network of claim 24 wherein the
2 second device provides a delay signal to the first buffer and delays the first signal
3 until after the second signal and third signal switches.

1 26. A signal transmitting network comprised of
2 a device configured to:
3 receive a first, a second, a third, a fourth, and a fifth adjacent signals;
4 sense transitions of the first, the second, the third, the fourth, and the fifth
5 signals;
6 delay switching of the first signal in the event the second signal transitions at
7 the same time as the first signal;
8 delay switching of the third signal in the event the second and fourth signals
9 transition at the same time as the third signal; and
10 delay switching of the fifth signal in the event the fourth signal transitions at
11 the same time as the fifth signal.

1 27. A signal transmitting network comprised of:
2 a first device configured to:
3 receive a first, a second, a third, a fourth, and a fifth adjacent signals;
4 delay switching of the first signal in the event the second signal transitions at
5 the same time as the first signal;
6 delay switching of the third signal in the event the second and fourth signals
7 transition at the same time as the third signal; and
8 delay switching of the fifth signal in the event the fourth signal transitions at
9 the same time as the fifth signal; and
10 a second device configured to:
11 sense transitions of the first, the second, the third, the fourth, and the fifth
12 signals.

1 28. The signal transmitting network of claim 26 wherein
2 the first signal is delayed by a first buffer before switching;
3 the second signal is delayed by a second buffer before switching;

4 the third signal is delayed by a third buffer before switching;
5 the fourth signal is delayed by a fourth buffer before switching; and
6 the fifth signal is delayed by a fifth buffer before switching.

1 29. The signal transmitting network of claim 28 wherein
2 sensing is performed by a sensing and delay circuit.

1 30. The signal transmitting network of claim 29 wherein
2 the sensing and delay circuit provides a delay signal to the first buffer and delays the
3 first signal until the second signal switches;
4 the sensing and delay circuit provides a delay signal to the third buffer and delays the
5 third signal until the second and the fourth signal switch; and
6 the sensing and delay circuit provides a delay signal to the fifth buffer and delays the
fifth signal until the fourth signal switches.

1 31. An apparatus for transmitting signals in a network comprised of:
2 means for receiving a first signal and a second adjacent signal;
3 means for sensing simultaneous transition of the first signal and the second signal;
4 means for delaying the first signal for a time period;
5 means for switching the second signal; and
6 means for switching the first signal after the time period.

1 32. The apparatus claim 31 wherein
2 the means for sensing is performed by a sensing and delay circuit.

1 33. The apparatus of claim 31 wherein
2 the first signal is delayed by a first buffer before switching; and
3 the second signal is delayed by a second buffer before switching.

1 34. The apparatus of claim 33 wherein
2 the means for sensing is performed by a sensing and delay circuit.

35. The apparatus of claim 34 wherein
the sensing and delay circuit provides a delay signal to the first buffer and delays the
first signal until after the second signal switches.

36. The apparatus of claim 31 further comprising:
means for receiving a third signal wherein the first signal is adjacent to the second and
the third signal;
means for sensing simultaneous transition of the third signal with the first signal;
means for delaying the first signal for a time period;
means for switching the third signal; and
means for switching the first signal after the time period.

37. The apparatus of claim 36 wherein
the means for sensing is performed by a sensing and delay circuit.

38. The apparatus of claim 37 wherein
the first signal is delayed by a first buffer before switching;
the second signal is delayed by a second buffer before switching, and
the third signal is delayed by a third buffer before switching.

39. The apparatus of claim 38 wherein
the means for sensing is performed by a sensing and delay circuit.

40. The apparatus of claim 39 wherein
the sensing and delay circuit provides a delay signal to the first buffer and delays the
first signal until after the second signal and third signal switches.

41. An apparatus of transmitting signals in a network comprising:
means for receiving a first, a second, a third, a fourth, and a fifth adjacent signals;
means for sensing transitions of the first, the second, the third, the fourth, and the fifth
signals;

5 means for delaying switching of the first signal in the event the second signal
6 transitions at the same time as the first signal;
7 means for delaying switching of the third signal in the event the second and fourth
8 signals transition at the same time as the third signal; and
9 means for delaying switching of the fifth signal in the event the fourth signal
10 transitions at the same time as the fifth signal.

1 42. The apparatus of claim 41 wherein
2 the means for sensing is performed by a sensing and delay circuit.

1 43. The apparatus of claim 41 wherein
2 the first signal is delayed by a first buffer before switching;
3 the second signal is delayed by a second buffer before switching;
4 the third signal is delayed by a third buffer before switching;
5 the fourth signal is delayed by a fourth buffer before switching; and
6 the fifth signal is delayed by a fifth buffer before switching.

1 44. The apparatus of claim 43 wherein
2 the means for sensing is performed by a sensing and delay circuit.

1 45. The apparatus of claim 44 wherein
2 the sensing and delay circuit provides a delay signal to the first buffer and delays the
3 first signal until the second signal switches;
4 the sensing and delay circuit provides a delay signal to the third buffer and delays the
5 third signal until the second and the fourth signal switch; and the sensing and
6 delay circuit provides a delay signal to the fifth buffer and delays the fifth
7 signal until the fourth signal switched.